The listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

- 1. (Canceled)
- 2. (Currently Amended) An EL-display A semiconductor device having at least one thin film transistor, said thin film transistor comprising:
- a thin film transistor comprising a semiconductor film formed over a substrate having an insulating surface, said semiconductor film comprising at least a channel region;
- a first insulating film comprising a thermal an oxide film of said semiconductor film thereon:
- a second insulating film comprising a deposition film over said semiconductor film; and
- a gate electrode formed adjacent to said channel region of said semiconductor film.

wherein said first insulating film contains at least one halogen element selected from fluorine and chlorine, and

wherein said semiconductor film contains hydrogen atoms at a concentration of 1 \times 10¹⁷ cm⁻³ to 1 \times 10²¹ cm⁻³, and oxygen atoms at a concentration of 2 \times 10¹⁹ cm⁻³ or less.

3. (Currently Amended) A semiconductor device according to claim 2, wherein said semiconductor film further contains a halogen element at a concentration of 1 x $10^{15} \text{ cm}^{-3} \text{ to } 1 \times 10^{20} \text{ cm}^{-3}$.

- 4. (Currently Amended) A semiconductor device according to claim 2, wherein said semiconductor film further contains a metal element which promotes crystallization of silicon at a concentration of 5 x 10¹⁹ cm⁻³ or less.
- 5. (Currently Amended) A semiconductor device according to claim 2 wherein 2, wherein said first insulating film includes at least one halogen element selected from fluorine and chlorine.
- 6. (Currently Amended) A semiconductor device according to claim 2, wherein said channel region contains carbon and nitrogen at a concentration of 2 x 10¹⁹ cm⁻³ or less, respectively.
- 7. (Currently Amended) An EL display A semiconductor device having at least one thin film transistor comprising:
- a thin film transistor comprising a crystalline semiconductor film formed on a substrate having an insulating surface, said crystalline semiconductor film having at least a channel region;

at least one gate insulating film adjacent to said crystalline semiconductor film, said one gate insulating film comprising thermal an oxide of said crystalline semiconductor film;

a gate electrode adjacent to said channel region of said crystalline semiconductor film with said gate insulating film interposed therebetween;

an inorganic interlayer insulating film over said crystalline semiconductor film and said gate electrode; and

an organic film over said inorganic interlayer insulating film,

wherein said channel region contains carbon and nitrogen at a concentration of 2 x 10¹⁹ cm⁻³ or less, respectively, and

wherein said <u>crystalline</u> semiconductor film contains a halogen element at a concentration of 1 x 10^{15} cm⁻³ to 1 x 10^{20} cm⁻³, and oxygen atoms at a concentration of 2 x 10^{19} cm⁻³ or less.

- 8. (Currently Amended) A <u>semiconductor</u> device according to claim 7, wherein said semiconductor film further contains hydrogen atoms at a concentration of 1 x 10^{17} cm⁻³ to 1 x 10^{21} cm⁻³.
- 9. (Currently Amended) A <u>semiconductor</u> device according to claim 7, wherein said semiconductor film further contains a metal element which promotes crystallization of silicon at a concentration of 5 x 10¹⁹ cm⁻³ or less, <u>less.</u>
- 10. (Currently Amended) A <u>semiconductor</u> device according to claim 7, wherein said inorganic interlayer insulating film <u>semprising comprises</u> one or <u>a plurality of films</u> selected from a silicon oxide film, a silicon nitride film, and a lamination film thereof.
- 11. (Currently Amended) A <u>semiconductor</u> device according to claim 7, wherein said organic film <u>comprising comprises</u> a resin material.
- 12. (Currently Amended) A <u>semiconductor</u> device according to claim 7, wherein said gate insulating film includes at least one halogen element selected from fluorine and chlorine.
- 13. (Currently Amended) A <u>semiconductor</u> device according to claim 7, wherein said halogen element is selected from the group consisting of fluorine, chlorine, and a mixture of fluorine and chlorine.

14. (Canceled)

- 15. (Currently Amended) An EL display A semiconductor device having-at least one thin film transistor, said thin film transistor comprising:
- <u>a thin film transistor comprising</u> a semiconductor film formed over a substrate having an insulating surface, said semiconductor film comprising at least a channel region;
- a first insulating film comprising a thermal an oxide film of said semiconductor film thereon;
- a second insulating film comprising a deposition film over said semiconductor film; and
- a gate electrode formed adjacent to said channel region of said semiconductor film,

wherein said first insulating film contains at least one halogen element selected from fluorine and chlorine, and

wherein said semiconductor film contains a metal element which promotes crystallization of silicon at a concentration of 5 x 10^{19} cm⁻³ or less, and oxygen atoms at a concentration of 2 x 10^{19} cm⁻³ or less.

- 16. (Currently Amended) A <u>semiconductor</u> device according to claim 15, wherein said semiconductor film further contains hydrogen atoms at a concentration of 1×10^{17} cm⁻³ to 1×10^{21} cm⁻³.
- 17. (Currently Amended) A <u>semiconductor</u> device according to claim 15, wherein said semiconductor film further contains a halogen element at a concentration of 1 x 10^{15} cm⁻³ to 1 x 10^{20} cm⁻³.
- 18. (Currently Amended) A <u>semiconductor</u> device according to claim 15, wherein said first insulating film includes at least one halogen element selected from fluorine and chlorine.

- 19. (Currently Amended) A semiconductor device according to claim 15, wherein said metal element is one or a plurality of elements selected from the group consisting of Fe, Co, Ni, Ru, Rh, Pd, Os, Ir, Pt, Cu and [[Au]] Au.
- (Currently Amended) A semiconductor device according to claim 15, 20. wherein said channel region contains carbon and nitrogen at a concentration of 2 x 10¹⁹ cm⁻³ or less, respectively.
- 21. (Currently Amended) An EL display A semiconductor device having at least one thin film transistor comprising:
- a thin film transistor comprising a crystalline semiconductor film formed on a substrate having an insulating surface, said crystalline semiconductor film having at least a channel region;

at least one gate insulating film adjacent to said crystalline semiconductor film, said one gate insulating film comprising thermal an oxide of said crystalline semiconductor film:

a gate electrode adjacent to said channel region of said crystalline semiconductor film with said gate insulating film interposed therebetween;

an inorganic interlayer insulating film over said crystalline semiconductor film and said gate electrode; and

an organic film over said inorganic interlayer insulating film,

wherein said channel region contains carbon and nitrogen at a concentration of 2 x 10¹⁹ cm⁻³ or less, respectively, and

wherein said crystalline semiconductor film contains a halogen element at a concentration of 1 x 10¹⁵ cm⁻³ to 1 x 10²⁰ cm⁻³, and hydrogen atoms at a concentration of 1 x 10^{17} cm⁻³ to 1 x 10^{21} cm⁻³.

- 22. (Currently Amended) A <u>semiconductor</u> device according to claim 21, wherein said semiconductor film further contains oxygen atoms at a concentration of 2 x 10^{19} cm⁻³ or less.
- 23. (Currently Amended) A <u>semiconductor</u> device according to claim 21, wherein said semiconductor film further contains a metal element which promotes crystallization of silicon at a concentration of 5×10^{19} cm⁻³ or less.
- 24. (Currently Amended) A <u>semiconductor</u> device according to claim 21, wherein said inorganic interlayer insulating film <u>comprising comprises</u> one or <u>a plurality of films</u> selected from a silicon oxide film, a silicon nitride film, and a lamination film thereof.
- 25. (Currently Amended) A <u>semiconductor</u> device according to claim 21, wherein said organic film <u>comprising comprises</u> a resin material.
- 26. (Currently Amended) A <u>semiconductor</u> device according to claim 21, wherein said gate insulating film includes at least one halogen element selected from fluorine and chlorine.
- 27. (Currently Amended) A <u>semiconductor</u> device according to claim 21, wherein said halogen element is selected from the group consisting of fluorine, chlorine, and a mixture of fluorine and chlorine.

28. (Canceled)

29. (Currently Amended) An EL display A semiconductor device having at least one thin film transistor, said thin film transistor comprising:

a thin film transistor comprising a semiconductor film formed over a substrate having an insulating surface, said semiconductor film comprising at least a channel region;

a first insulating film comprising a thermal an oxide film of said semiconductor film thereon:

a second insulating film comprising a deposition film over said semiconductor film; and

a gate electrode formed adjacent to said channel region of said semiconductor film,

wherein said first insulating film contains at least one halogen element selected from fluorine and chlorine, and

wherein said semiconductor film contains a metal element which promotes crystallization of silicon at a concentration of 5 x 10¹⁹ cm⁻³ or less, and hydrogen atoms at a concentration of 1 x 10¹⁷ cm⁻³ to 1 x 10²¹ cm⁻³.

- (Currently Amended) A semiconductor device according to claim 29, wherein said semiconductor film further contains oxygen atoms at a concentration of 2 x 10¹⁹ cm⁻³ or less.
- (Currently Amended) A semiconductor device according to claim 29, 31. wherein said semiconductor film further contains a halogen element at a concentration of 1 x 10^{15} cm⁻³ to 1 x 10^{20} cm⁻³.
- (Currently Amended) A semiconductor device according to claim 29, 32. wherein said first insulating film includes at least one halogen element selected from fluorine and chlorine.

- (Currently Amended) A semiconductor device according to claim 29, 33. wherein said metal element is one or a plurality of elements selected from the group consisting of Fe, Co, Ni, Ru, Rh, Pd, Os, Ir, Pt, Cu and [[Au]] Au.
- (Currently Amended) A semiconductor device according to claim 29, 34. wherein said channel region contains carbon and nitrogen at a concentration of 2 x 10¹⁹ cm⁻³ or less, respectively.
- 35. (Currently Amended) An EL display A semiconductor device having at least one thin film transistor comprising:
- a thin film transistor comprising a crystalline semiconductor film formed on a substrate having an insulating surface, said crystalline semiconductor film having at least a channel region;

at least one gate insulating film adjacent to said crystalline semiconductor film, said one gate insulating film comprising thermal an oxide of said crystalline semiconductor film;

a gate electrode adjacent to said channel region of said crystalline semiconductor film with said gate insulating film interposed therebetween;

an inorganic interlayer insulating film over said crystalline semiconductor film and said gate electrode; and

an organic film over said inorganic interlayer insulating film,

wherein said channel region contains carbon and nitrogen at a concentration of 2 x 10¹⁹ cm⁻³ or less, respectively, and

wherein said crystalline semiconductor film contains a metal element which promotes crystallization of silicon at a concentration of 5 x 10¹⁹ cm⁻³ or less, and a halogen element at a concentration of 1 x 10¹⁵ cm⁻³ to 1 x 10²⁰ cm⁻³.

- 36. (Currently Amended) A <u>semiconductor</u> device according to claim 35, wherein said semiconductor film further contains oxygen atoms at a concentration of 2 x 10^{19} cm⁻³ or less.
- 37. (Currently Amended) A <u>semiconductor</u> device according to claim 35, wherein said semiconductor film further contains hydrogen atoms at a concentration of 1×10^{17} cm⁻³ to 1×10^{21} cm⁻³.
- 38. (Currently Amended) A <u>semiconductor</u> device according to claim 35, wherein said inorganic interlayer insulating film comprising <u>comprises</u> one or <u>a</u> plurality <u>of</u> films selected from a silicon oxide film, a silicon nitride film, and a lamination film thereof.
- 39. (Currently Amended) A <u>semiconductor</u> device according to claim 35, wherein said organic film <u>comprising comprises</u> a resin material.
- 40. (Currently Amended) A <u>semiconductor</u> device according to claim 35, wherein said gate insulating film includes at least one halogen element selected from fluorine and chlorine.
- 41. (Currently Amended) A <u>semiconductor</u> device according to claim 35, wherein said halogen element is selected from the group consisting of fluorine, chlorine, and a mixture of fluorine and chlorine.
- 42. (Currently Amended) A <u>semiconductor</u> device according to claim 35, wherein said metal element is one or a plurality <u>of</u> elements selected from the group consisting of Fe, Co, Ni, Ru, Rh, Pd, Os, Ir, Pt, Cu and Au

43. (Canceled)